

Parallel Numerical Creation of 2-parametric Bifurcation Diagram of Nonlinear Oscillators

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Abstract: This paper presents the numerical creation of 2-parametric bifurcation diagrams of nonlinear oscillators with a simple iterative algorithm, which can be easily parallelized. The parallel algorithm was tested with two simple well-known nonlinear oscillators, the Van der Pol oscillator and the Duffing-Holmes oscillator. It was examined how the resolution (number of iterations) affects the speedup and the efficiency. The test results show that a relative good speed up with a good efficiency could be achieved even using a simple desktop.

Keywords: parallel computing, bifurcation diagram, numerical analysis, nonlinear oscillator, Maple

1. Introduction

Bifurcation diagram is a very useful tool to study nonlinear dynamical systems. It shows the systems behaviour as a system parameter is varied [1]. In most cases the bifurcation diagram of a single parameter is calculated [2]-[9], but a system can have more bifurcation parameters [10]. In order to examine the effect of more bifurcation parameters at the same time a 2 parametric or 3D bifurcation diagram can be created [11]. However constructing a detailed 3D bifurcation diagram can be time consuming even in case of simple systems, as a lot of numerical calculation is necessary. These calculations can be fastened using more processor cores [12]. The aim of this research was to create 3D bifurcation diagrams within a reasonable time using simple PC-s.

The paper first presents some examples using 3D bifurcation diagrams found in the literature, then the sequential and parallel numerical creation of 3D bifurcation

diagrams are described, which is followed by some test results. The paper concludes with further development tasks.

2. Creation of bifurcation diagram with more parameters

In the literature some examples of simulations using 3D bifurcation diagrams and methods for calculating and visualizing bifurcation surfaces of more parameters can be found.

In [11], the creation of the 3D bifurcation diagram is presented with a predatorprey model. The bifurcation points are calculated with the Newton-Raphson method. Then the surface diagram is created with triangulation.

In [13], the use of different 3D bifurcation diagram is presented with population model examples, like ecosystem models and predator-prey models. A 3D bifurcation diagram with 2 state variables and a bifurcation parameter is used to determine the stability of the systems. 3D bifurcation diagrams with 3 different parameters are calculated to examine the equilibrium points and the type of bifurcations.

Reference [14] proposes an efficient algorithm to calculate and visualize three dimensional bifurcation surfaces. The algorithm was tested with a socio-economic model, a metabolic network and a food-chain model.

In [15], an analytical method based on resultant is presented to calculate bifurcation surfaces. The presented method is suitable for medium size systems and can be used to detect Hopt bifurcations and some higher codimension bifurcations too.

More parametric bifurcation diagrams have been used to study electrical systems. In [16], ferroresonance of power systems is predicted with 3D bifurcation diagrams. The 3D bifurcation diagram is produced with creating as many 2D bifurcation diagrams as the second parameter range. The 2D bifurcation diagram is created with repeating time-domain simulations followed by frequency-domain sampling of the same output to determine its periodicity. The creation of bifurcation diagram was speed up using a 50 core parallel computer [17].

In [18], the bifurcation analysis of a permanent magnet synchronous motor was achieved using 2D bifurcation curves and 3D bifurcation surfaces. The bifurcation surfaces were created from bifurcation curves calculated with different values of the third parameter.

In [19], the 3D bifurcation diagram of a simple electrical system with a nonlinear Tunnel diode is created using Maple's *implicitplot* command.

2D and 3D bifurcation diagrams can be used to examine biochemical reactions too. In [20], stable states of a protometabolic network are studied with 2D and 3D bifurcation diagrams.

The creation of 3D bifurcation diagram can be very time consuming [17]. The aim of this research was to expand a fast and efficient algorithm used to calculate 2D bifurcation diagrams in a way to create 3D bifurcation diagrams even on simple PC-s within a reasonable time.

2.1. The algorithm to create the 3D bifurcation diagram

The 2D bifurcation diagram can be created with a simple iterative algorithm based on the algorithm described in [21]. The flowchart of the algorithm can be seen in Figure 1.



Figure 1. Algorithm to create the bifurcation diagram

The 3D bifurcation diagram can be created if the 2D bifurcation diagrams are calculated with every value of a different bifurcation parameter and the results are displayed in a 3D plot (Figure 2).



Figure 2. Algorithm to create the 3D bifurcation diagram

In this study the numerical 3D bifurcation diagram of the forced Duffing-Holmes and the forced Van der Pol oscillator is created. Maple was used as it is a powerful tool for both symbolic [22] and numerical calculations[23]. The equation of the Duffing-Holmes oscillator is:

$$\frac{d^2}{dt^2}x(t) + \delta \frac{d}{dt}x(t) + \beta x(t) + \alpha x(t)^3 = \gamma \cos(\omega t)$$
(1)

In this study parameters $\alpha=1$, $\beta=-1$ and $\omega=1$ were chosen as constants and δ and γ are the bifurcation parameters. The bifurcation diagram can be seen in Figure 3.



Figure 3. 3D bifurcation diagram of the Duffing-Holmes oscillator

From the diagram the bifurcation regions can be observed, which are:

- γ=0
- δ=0
- the region between $\gamma = 4/3 \times \delta + 0.2$ and $\gamma = 0.6 \times \delta + 0.2$

To test the accuracy of the bifurcation diagram some Poincare sections were created, which can be seen in Figure 4.



Figure 4. Poincaré sections of the Duffing-Holmes oscillator (γ =0.1 and δ =0, γ =0.2 and δ =0.8, γ =0.6 and δ =0.5)

It can be seen, that the Poincaré sections also show the chaotic behaviour at the same parameter values as the bifurcation diagrams. When $\gamma=0.1$ and $\delta=0$ and $\gamma=0.6$ and $\delta=0.5$ there is a chaotic oscillation and in case of $\gamma=0.2$ and $\delta=0.8$ there is a harmonic oscillation.

The equation of the forced Van der Pol oscillator is:

$$\frac{d^2}{dt^2}x(t) = \mu(1 - x(t)^2)\frac{d}{dt}x(t) - x(t) + A\cos(\omega t)$$
(2)

In this study $\omega = 2\pi/10$ and μ and A are the bifurcation parameters. The 3D bifurcation diagram can be seen in Figure 5.



Figure 5. 3D bifurcation diagram of the Van der Pol oscillator

From the diagram the bifurcation regions can be observed, which are:

- the region $\mu \leq 10 \times A$
- between $\mu = 2.5 \times A$, A = [1.2-2] and $\mu = 10$, A = [0,1.2] and $\mu = 15$, A = 0

If $\mu = 0$ there is a subharmonic oscillation. Some Poincaré sections were created to test the accuracy of the bifurcation diagram with a different system too (Figure 6).



Figure 6. Poincaré sections of the Van der Pol oscillator (μ =6 and A=0.5, μ =3 and A=0.2 and μ =10 and A=1.5)

It can be seen that similarly the previous system the Poincaré sections shows the system behaviour according to the 3D bifurcation diagram: when $\mu = 6$, A=0.5 and $\mu = 3$ and A=0.2 there is a chaotic limit cycle and when $\mu = 10$ and A=1.5 there is a subharmonic oscillation.

As the same iterative algorithm is used with every values of the bifurcation parameters this task can be easily parallelized.

2.2. Parallel algorithm

The numerical creation of the 2 parametric bifurcation diagram can be parallelized with a similar algorithm presented in [24]. For parallelization Maple's Grid programming model was used. There is a master node to supervise the slave nodes and to collect the results. The slave nodes calculate the data for the bifurcation diagram for each bifurcation parameter and send the results to the master node. The flow chart of the parallel algorithm can be seen in Figure 7 and the Pseudo code of the Maple program can be seen in Figure 8.



Figure 7. Flow chart of the Grid Programming model (SIMD parallel algorithm)

```
dist lc := \mathbf{proc}()
#define variables
#setup nodes
this node := MvNode() :
active nodes := NumNodes() -1:
#initialization
#set master node
if this node = 0 then
   for iNode from 1 to active nodes do
#receive results from slave nodes
  end do:
#set slave nodes
else
for i from 1 to nops(delta) do
imod := i \mod active \ nodes:
if imod = 0 then imod := active nodes end if:
if this node = imod then
#iterative algorith to calculate the bifurcation
#diagram for each parameter value
end if
#send results to master node
end if :
end proc:
```

Figure 8. Pseudo code of the parallel Maple program

3. Creation of bifurcation diagrams with more parameters

The tests were carried out on a PC with an Intel[®] Core[™] i5-4460 Processor 3.2 GHz and 16 GB RAM. The processor has 4 cores

The speedup, the efficiency and the overhead of the program were calculated with the following formulas [25]-[27]:

$$S = \frac{T_s}{T_p} \tag{3}$$

$$E = \frac{S}{n} \tag{4}$$

$$0 = \frac{T_p - T_u}{T_p} \tag{5}$$

where T_s is the sequential calculation time, T_p is the parallel running time, T_u is the time of the useful work (calculation time of a 2D bifurcation diagram), n is the number of cores.

To change the resolution of the bifurcation diagram the number of iteration was varied for both parameters:

$$resolution = \frac{1}{N} \tag{6}$$

where N is the number of iterations. The number of iterations for each resolution can be seen in Table 1. G is parameter of the iterative algorithm to create a single bifurcation diagram (γ and μ) and D is the parameter for the number of bifurcation diagrams in 3D (δ and A).

Table 1. Number of iterations as resolution of the bifurcation parameters (G and
D) is changed

| | D | 11 | 26 | 51 | 101 | 251 | 501 | 1001 |
|------|---|-------|-------|-------|--------|--------|--------|---------|
| G | | | | | | | | |
| 11 | | 121 | 286 | 561 | 1111 | 2761 | 5511 | 11011 |
| 26 | | 286 | 676 | 1326 | 2626 | 6526 | 13026 | 26026 |
| 51 | | 561 | 1326 | 2601 | 5151 | 12801 | 25551 | 51051 |
| 101 | | 1111 | 2626 | 5151 | 10201 | 25351 | 50601 | 101101 |
| 251 | | 2761 | 6526 | 12801 | 25351 | 63001 | 125751 | 251251 |
| 501 | | 5511 | 13026 | 25551 | 50601 | 125751 | 251001 | 501501 |
| 1001 | | 11011 | 26026 | 51051 | 101101 | 251251 | 501501 | 1002001 |

3.1. Duffing-Homes oscillator

In case of the Duffing-Holmes oscillator set [0,1] was given for both bifurcation parameters. The calculation times as the resolution of γ is varied can be seen in Figure 9.



Figure 9. Calculation times versus the resolution (different resolution for G) in case of the Duffing-Holmes oscillator

It can be seen that the calculation time increases as the resolution is increased. The calculation time of the parallel algorithm is less than 1000 s at the highest resolution. The speedup and the efficiency are shown in Figure 10.



Figure 10. Speedup and efficiency versus the resolution (different resolution for G) in case of the Duffing-Holmes oscillator

From Figure 10 the following can be derived:

- When G=10 and D is small S<2 and E<50%. When G is increased both of them increases. When D=1000 S>3.6 and E>91%. It is the best result achieved.
- When G=25 and G is small S<2.5 and E<60%. When D is increased both increases fast and when D>100 an approximately constant S=3.58 and E=90% can be achieved.
- When G=50 and G is small S=2.5 and E=60%. When D is increased first there is a fast increase in G and D. The best results could be achieved, when D=50, in this case S>3.6 and E>91%. When D is increased further first there is a decrease in both. An average S=3.5 and S=89% could be achieved.
- When G=100 and D is small S=2.8 and E=70%. When D is increased there is an increase in both. The best result could be achieved, when D=100. In this case S=3.6 and E=90%. When D is further increased both of them decreases, but E>85% remains.
- When G=250 and D is small S=2.9 and E=72%. When D is increased both of them increases (till D=50), then they decrease (till D=250) and then they increase again till S=3.25 and E=81%. The best results could be achieved when D=50 and the worst results when D=250.
- When G=500 and D is increased (till D=50) S and E increase, after they decrease (till D=250) and after a small increase they remain constant (S=3.22 and E=80%).
- When G=1000 and D is increased after a short increase S decreases from 3.5 to 2.8 and E decrease from 87% to 72%. At this resolution lack of memory also occurred.

The overhead is shown is Figure 11.



Figure 11. Overhead versus the resolution (different resolution for G) in case of the Duffing-Holmes oscillator

From Figure 11 the following can be derived:

- When the resolution is small the overhead is high (40-60%), which means the communication time between the nodes is high compared to the useful calculation time
- The overhead increases (till almost 30%) when G=1000 and the resolution is increased
- The overhead is increased till 20% when G=250 and G=500 and the resolution is increased
- All the other cases the overhead is 10%, which means only 10% of the total running time is spent on the communication

The effect of different G and D values in case of the same number of iterations to efficiency and overhead was also compared. The results can be seen in Figure 12.



Figure 12. Comparison of efficiency and overhead with different G and D values at the same number of iterations in case of the Duffing-Holmes oscillator

It can be seen that except the smallest resolution the speedup and efficiency were better, when G<D. It means that it is more effective to calculate a lot of low resolution 2D bifurcation diagram in parallel, that to calculate a low amount of high resolution 2D bifurcation diagrams.

The average difference is 4%. The smallest difference is at resolution 500×250 and 1000×250 , it is <20% is both cases. The difference was small (<3%) in resolution 10×25 , 25×50 , 25×100 , 50×250 and 100×1000 . The maximum difference is 17.27% (means 0.69 less speedup), which is at resolution 10×250 .

The overhead is also much higher at this resolution, when G>D. The difference is also high at resolution 10×500 (8.84%) and 10×1000 (12%). It also shows that it is not efficient to calculate small number of detailed 2D bifurcation diagrams in parallel, as communication can be time consuming.

Figure 13. shows the average speedup, efficiency and overhead versus the average number of iterations.



Figure 13. Average speedup, efficiency and overhead versus the average number of iterations in case of the Duffing-Holmes oscillator

It can be seen that S and E increases till the number of iterations is 12000, after it decreases. The speedup is still >3 and the efficiency is 77% with 500000 iterations. The average overhead first is above 40% and when the resolution is increased (around 3000 iterations) it desreases under 20%.

3.2. Van der Pol oscillator

For this test A=[0,2] and μ =[0,15] sets were given. The calculation times as the resolution of μ is varied can be seen in Figure 14.



Figure 14. Calculation times versus the resolution (different resolution for G) in case of the Van der Pol oscillator

It can be seen that the calculation time increases as the resolution is increased. The calculation time of the parallel algorithm is less than 1500 s at the highest resolution. The speedup and the efficiency are shown in Figure 15.



Figure 15. Speedup and efficiency versus the resolution (different resolution for G) in case of the Van der Pol oscillator

From Figure 15 the following can be derived:

• When G=10 and D is small S<2 and E<50%. When G is increased both of them increases till S=3.2 and E=81%.

- When G=25 and G is small S=2.5 and E=61%. When D is increased both increases fast and when D>250 an approximately constant S=3.2 and E=81% can be achieved.
- When G=50 and G is small S=2.7 and E=67%. When D is increased till 500 both of them increases till S=3.28 and E=82%. When D is further increased there is a small decrease in E and S.
- When G=100 and D is small S=2.9 and E=72%. When D is increased till 500 there is an increase in both till S=3.3 and E=82%. When D is further increased there is a small decrease in both of them.
- When G=250 and D is small S=2.9 and E=73%. When D is increased both of them increases till D=100 (S=3.4 and E=86%). When D is further increased first there is a small decrease (till D=500) and then a small increase in both.
- When G=500 and D is increased (till D=50) S and E increase. This is the best result achieved: S=3.45 and E=86%. After that they decrease till S=3.1 and E=77%.
- When G=1000 and D is increased after a short increase S decreases from 3.3 to 3.2 and E decreases from 83% to 80%.

The overhead is shown is Figure 16.



Figure 16. Overhead versus the resolution (different resolution for G) in case of the Van der Pol oscillator

From Figure 16 the following can be derived:

- When the resolution is small the overhead is high (40-55%), which means the communication time between the nodes is high compared to the useful calculation time
- The overhead increases (till almost 25%) when G=500 as the resolution is increased
- The overhead is increased above 20% when G=1000 and the resolution is increased
- All the other cases the overhead is <20%, which means less than 20% of the total running time is spent on the communication

The effect of different G and D values in case of the same number of iterations to efficiency and overhead was also compared. The results can be seen in Figure 17.

It can be seen that most cases the speedup and efficiency were better (and the overhead lower), when G>D, only at some higher resolution cases were the efficiency better in case of G<D. It means that similarly to the Duffing-Holmes oscillator it is more efficient to create a lot of low resolution 2D diagrams, than to create a smaller number of higher resolution 2D diagrams in parallel.

The average difference is 2.3%. The smallest difference is at resolution 100×1000 and it is <0.15%. The difference is less than 4% in almost all cases except resolution 250×10 (6.04%) and resolution 500×50 (4.4%). The difference in overhead is also higher at these resolutions. The difference in efficiency and overhead was much less than the difference with the Duffing-Holmes oscillator, which means that the tasks could be divided between the nodes more effectively.



Figure 17. Comparison of efficiency and overhead with different G and D values at the same number of iterations in case of Van der Pol oscillator

Figure 18 shows the average speedup, efficiency and overhead versus the number of iterations.



Figure 18. Average speedup, efficiency and overhead versus the number of iterations in case of the Van der Pol oscillator

It can be seen that S and E increases till the number of iterations is 25000, after it is nearly constant (S=3.3 and E=83%) till number of iterations is 250000, ather that it decreases. The speedup is still >3.1 and the efficiency is 78%. The average overhead is first >40% and when the resolution is increased (around 6000 iterations) it desreases under 20%.

4. Conclusions and further development

The parallel numerical creation of a 3D bifurcation diagram of two simple nonlinear oscillators was carried out with a simple and fast iterative algorithm. An average 3-fold speedup could be achieved with average 76% efficiency in both cases. The best results could be achieved with the Duffing-Holmes oscillator, when the number of iteration was 12000 (3.5-fold speedup and 87% efficiency). A better speedup and efficiency could be achieved with the Duffing-Holmes oscillator when the number of iteration was between 2000 and 50000 as it can be seen in Figure 19. At all other resolutions the results were better with the Van der Pol oscillator. The average overhead was high till 3000 iterations (30-40%), then it was <20% till 100000 iterations in both cases. When the resolution is further increased the average overhead increases till 20-30% (Figure 19).



Figure 19. The average speedup, efficiency and overhead versus the number of iterations

With the presented algorithm a high resolution 3D bifurcation diagram can be created of simple systems very effectively with simple PC-s with low number of cores.

Next task is the better visualization of the results, which means using surface plot instead of point plot. Some initial experiments have already been carried out creating surface plots using Maple, but for visualization there was not enough memory.

Other task in the future is utilizing supercomputers to create even more detailed 3D bifurcation diagram of more complex systems and to create other high-calculation time diagrams, like frequency spectrum maps in parallel

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